2019/TDC/EVEN/CSCHC-201T/044

TDC (CBCS) Even Semester Exam., 2019

COMPUTER SCIENCE

(2nd Semester)

Course No. : CSCHCC-201T

(Computer System Architecture)

Full Marks : 70 Pass Marks : 28

Time: 3 hours

The figures in the margin indicate full marks for the questions

Answer **all** questions

Unit—I

- **1.** Answer any *two* questions from the following : 2×2=4
 - (a) List the truth table of a three-variable exclusive OR (odd) function :

x A B C

(b) Simplify the boolean functions using four-variable maps :

F(*A*, *B*, *C*, *D*) (0, 1, 2, 4, 5, 7, 11, 15)

(c) What is multiplexer? Give example.

J9/2166

(Turn Over)

(2)

(a)	Explain clocked Rs flip-flop and <i>T</i> flip-flop.	5
(b)	Design a full-adder circuit using two half-adder and an OR gate.	5
OR		
(a)	Simplify the boolean function F together with the don't care conditions d in	
	(i) sum of products form(ii) product of sums form	
	<i>F</i> (<i>w</i> , <i>x</i> , <i>y</i> , <i>z</i>) (0, 1, 2, 3, 7, 8, 10)	
	<i>d</i> (<i>w</i> , <i>x</i> , <i>y</i> , <i>z</i>) (5, 6, 11, 15)	5
	(a) (b) (a)	 (a) Explain clocked Rs flip-flop and T flip-flop. (b) Design a full-adder circuit using two half-adder and an OR gate. OR (a) Simplify the boolean function F together with the don't care conditions d in (i) sum of products form (ii) product of sums form F (w, x, y, z) (0, 1, 2, 3, 7, 8, 10) d(w, x, y, z) (5, 6, 11, 15)

- (b) Write short notes on the following : $2\frac{1}{2}+2\frac{1}{2}=5$
 - (i) Shift register
 - (ii) Binary counter

Unit—II

4. Answer any *two* of the following questions :

2×2=4

- (a) Convert $(41 \ 6875)_{10}$ to binary.
- (b) Find the 10's complement subtraction of 72532–13250.
- (c) How can floating point number be represented in computer system?

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(Continued)

(3)

- **5.** (a) Write an algorithm for addition and of signed subtraction magnitude numbers.
 - Represent the decimal number 8620 to (b)the following bases : 3
 - (i) BCD
 - (ii) Excess-3 code
 - *(iii)* 2421 code

OR

- Explain with an example Booth's **6.** (a) multiplication algorithm of signed 2's complement number. 7
 - What is the difference between fixed (b)3 point and floating representation?

Unit—III

- 7. Answer any *two* of the following questions : $2 \times 2 = 4$
 - Define micro-operation with example. (a)
 - Define hardwired control and micro-(b)programmed control
 - Write short notes on control unit. (c)

(4)

Describe the flowchart of instruction

cycle. Design a 4-bit bus system and draw the (b)diagram. OR Briefly define basic instruction formats. **9.** (a) (b)Briefly describe the functions of computer registers. UNIT—IV **10.** Answer any *two* of the following questions : What is control word? Give example. (a)Write down the purpose of stack (b)pointer. What is program counter? (c)What are the different addressing **11.** (a) modes? Explain with an example. Write down the difference between RISC (b)and CISC. J9**/2166**

8. (a)

J9/2166

7

(Continued)

5

5

3

7

 $2 \times 2 = 4$

7

3

12. Write a program to evaluate the arithmetic statement

 $X \quad \frac{A \quad B \quad C \quad (D \quad E \quad F)}{G \quad H \quad K}$

- (a) Using a general register, compute with three address instructions.
- (b) Using a general register, compute with two address instructions.5

Unit—V

13. Answer any *two* of the following questions :

2×2=4

5

5

- (a) What are start bit and stop bit?
- (b) What is interrupt?
- (c) Define DMA.
- **14.** (*a*) Explain DMA controller with block diagram.
 - (b) Explain the asynchronous mode of data transfer with diagram.5

OR

- **15.** (a) Explain with diagram, the functions of interrupt controller. 5
 - (b) What is the difference between isolated I/O and memory mapped I/O? What are the advantages and disadvantages of each? 3+2=5

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